

Attorney Docket No. 259/023

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re patent application of

Ji-Young KIM, et al.

Group Art Unit: 2814

Serial No. 10/759,239

Examiner: Long Pham

Filed: January 20, 2004

For: VERTICAL DOUBLE-CHANNEL SILICON-ON-INSULATOR TRANSISTOR AND  
METHOD OF MANUFACTURING THE SAME

**PETITION FOR EXTENSION OF TIME**  
**UNDER 37 C.F.R. § 1.136**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA. 22313-1450

Sir:

It is respectfully requested that an extension of time for the period indicated below be granted in accordance with the provisions of 37 C.F.R. §1.136 to take the action required in the application identified in caption, as reflected by the papers submitted herewith.

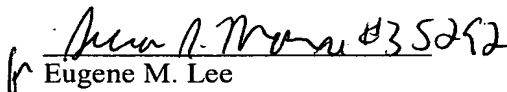
<input checked="" type="checkbox"/> First Month	\$120 (\$ 60)*
<input type="checkbox"/> Second Month	\$330 (\$165)*
<input type="checkbox"/> Third Month	\$570 (\$285)*
<input type="checkbox"/> Fourth Month	\$570 (\$285)*
<input type="checkbox"/> Fifth Month	\$570 (\$285)*

\*(Small Entity) TOTAL FEE: \$120.00

The Commissioner is hereby authorized to charge the necessary extension fees to our Credit Card as indicated on the attached form PTO-2038.

Respectfully submitted,

May 4, 2005  
Date

  
Eugene M. Lee  
Registration No. 32,039

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DEPOSIT ACCOUNT CHARGE AUTHORIZATION

If fee payment is enclosed, this amount is believed to be correct. However, the Director is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-1645.

Any additional fee(s) necessary to effect the proper and timely filing of the above-paper may also be charged to Deposit Account No. 50-1645.

05/05/2005 HAL111 00000091 10759239

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